



Catalyst Monitoring Operation – Discovery Series II								
Component/ System	Fault Codes	Monitoring Strategy Description	Malfunction Criteria	Threshold value	Secondary Parameter	Enable Conditions	Time Required	MIL Illumination
<b>Catalyst</b> Bank 1	P0420	oxygen storage capability	rear oxygen sensor amplitude exceeds the modelled amplitude of a borderline catalyst (1.75 x standard (Hydrocarbon - (HC) emissions))	> 0.4023	engine speed engine load	1200 < rpm < 1800 between 1.8 and 3.8 msec at 1200 Rpm to between 1.9 and 4.15 msec at 1800 rpm > 332 °C high range closed loop < 10.0 valid for > 0.8 sec	100 sec/ once per driving cycle	two driving cycles
Bank 2	P0430			> 0.4023	catalyst temperature (model) transfer gears fuel system status EVAP canister purge vapour factor enable conditions			

If the above table does not include details of the following enabling conditions: - IAT, ECT, vehicle speed range, and time after engine start-up then the state of these parameters has no influence upon the execution of the monitor.

Catalyst Monitoring Operation – Range Rover 38A								
Component/ System	Fault Codes	Monitoring Strategy Description	Malfunction Criteria	Threshold value	Secondary Parameter	Enable Conditions	Time Required	MIL Illumination
<b>Catalyst</b> Banks 1 and 2 (Dual catalyst deterioration)	P0420 P0430	oxygen storage capability	amplitude ratio of O2S, rear/front (1.5 x standard + 4K (HC emissions))	> 0.5 (min. 4 of 4 samples per cylinder bank)	engine speed engine load catalyst temperature (model) IAT	1000 < rpm < 2800 1.2< TL msec <4.0 > 300 °C > -9.75 °C high range closed loop < 10.0 > 69.12 sec	250 sec/ once per driving cycle	two driving cycles
Bank 1 or 2 (Single catalyst deterioration)				> 0.75 (min. 4 of 4 samples for one cylinder bank)	transfer gears fuel system status EVAP canister purge vapour factor time after start			

If the above table does not include details of the following enabling conditions: - IAT, ECT, vehicle speed range, and time after engine start-up then the state of these parameters has no influence upon the execution of the monitor.